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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of:
CHUNG et al.

Application No.: 10/056,367

Filed: January 25, 2002

For: INPUT BUFFER FOR MULTIPLE
DIFFERENTIAL I/O STANDARDS

Confirmation No. 3942

Group Art Unit: 2819

Examiner: Daniel D. Chang

Attorney Docket No.: 9818-060-999

6/A Amdt
9.16.03
A.W.cek

RESPONSE TO OFFICE ACTION MAILED JUNE 4, 2003

COMMISSIONER FOR PATENTS
P.O. BOX 1450
Alexandria, Virginia 22313-1450

Sir:

Applicants submit the following amendments and remarks in response to the Office
Action mailed June 4, 2003, in the above-identified application.

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In Claims:

Rewrite the pending claims to read as follows:

1. (Original) An input buffer circuit comprising:
first and second differential input terminals for receiving first and second input signals
respectively, the difference between the input signals providing a differential input signal;
a first differential amplifier circuit having a first input coupled to the first differential
input terminal and a second input coupled to the second differential input terminal, the first
differential amplifier circuit being configured to generate a first logic level signal at an output
node when the differential input signal is provided in accordance with a first digital I/O
standard;
a second differential amplifier circuit having a first input coupled to the first
differential input terminal and a second input coupled to the second differential input
terminal, the second differential amplifier circuit being configured to generate a second logic